

10/6/18, 710

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO. 1687.1004	APPLICATION NO. Not Yet Assigned
LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)		FIRST NAMED INVENTOR Koichiro ISHIBASHI		
		FILING DATE Concurrently Herewith	GROUP ART UNIT Unknown 2818	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
AA						
AB						
AC						
AD						
AE						
AF						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO
TH	AG	2001-345424	12/14/01 JAPAN			Abstract
	AH					
	AI					
	AJ					
	AK					
	AL					

OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

TH	AM	S. Narendra et al., "1.1V 1GHz Communications Router with On-Chip Body Bias 150nm CMOS," ISSCC 2002/SESSION 16/HIGH SPEED I/O 16.4, pp. 270, 271,466, February 2002.
TR	AN	Kanda et al., "Design Impact of Positive Temperature Dependence on Drain Current in Sub-1-V CMOS VLSIs," IEEE J. Solid-State Circuits, vol. 36, No. 10, pp. 1559-1564, October 2001.
	AO	

EXAMINER	TH-TR-HO	DATE CONSIDERED	June 2001
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			